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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/518,697	12/21/2004	Masaki Aoki	28951.2186	8032
53067 7590 09/19/2007 STEPTOE & JOHNSON LLP 1330 CONNECTICUT AVE., NW WASHINGTON, DC 20036			EXAMINER WILLIAMS, JOSEPH L	
			ART UNIT 2879	PAPER NUMBER
			MAIL DATE 09/19/2007	DELIVERY MODE PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

**Office Action Summary**

Application No.

10/518,697

Applicant(s)

AOKI ET AL.

Examiner

Joseph L. Williams

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 27 August 2007.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1,4,6 and 7 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1,4,6,7 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: \_\_\_\_\_

## **DETAILED ACTION**

### ***Continued Examination Under 37 CFR 1.114***

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 8/27/2007 has been entered.

### ***Claim Rejections - 35 USC § 102***

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 4, 6, and 7 are rejected under 35 U.S.C. 102(b) as being anticipated by Kato et al. (JP 2002-360535), of record by Applicant.

Regarding claim 1, Kato ('535) teaches in figure 1 and throughout the text a plasma display panel comprising: a front panel comprising: a first substrate (9); a first electrode (7) on the first substrate; a dielectric glass layer (6) covering the first electrode; and a protective film (5) on the dielectric glass layer, the protective film comprising magnesium oxide (MgO) and an additional oxide, said additional oxide comprising an element with an electronegativity of 1.4 or higher (property of silicon oxide) and having a negative charge including at least silicon oxide; and a back panel

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on a second substrate (no number) comprising: at least a second electrode (3); a barrier rib (2); and a phosphor layer (1R, 1G, 1B), wherein the protective film and the phosphor layer are arranged facing each other, and form a discharge space partitioned with a barrier rib between the front panel and the back panel.

Regarding claim 4, Kato ('535) teaches a method for producing a plasma display panel including: forming a first electrode on a first substrate; forming a dielectric glass layer to cover the first electrode; forming a protective film to cover the dielectric glass layer, the protective film comprising magnesium oxide (MgO) and an additional oxide, said additional oxide comprising an element with an electronegativity of 1.4 or higher and having a negative charge including silicon oxide; wherein the process of forming the protective film is vacuum evaporation (paragraph 13).

Regarding claim 6, Kato ('535) teaches the second electrode is positioned orthogonally to the first electrode.

Regarding claim 7, Kato ('535) teaches a second electrode on a second substrate, wherein the first electrode and the second electrode are arranged orthogonally to each other.

Claims 1, 4, 6, and 7 are rejected under 35 U.S.C. 102(b) as being anticipated by Daito (JP 2000-294153).

Regarding claim 1, Daito ('153) teaches in figure 1 and throughout the text a plasma display panel comprising: a front panel (100) comprising: a first substrate (1); a first electrode (4) on the first substrate; a dielectric glass layer (6) covering the first

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electrode; and a protective film (7 and 30) on the dielectric glass layer, the protective film comprising magnesium oxide (MgO) and an additional oxide, said additional oxide comprising an element with an electronegativity of 1.4 or higher (property of silicon oxide) and having a negative charge including at least silicon oxide; and a back panel (200) on a second substrate (2) comprising: at least a second electrode (8); a barrier rib (3); and a phosphor layer (9), wherein the protective film and the phosphor layer are arranged facing each other, and form a discharge space partitioned with a barrier rib between the front panel and the back panel.

Regarding claim 4, Daito ('153) teaches a method for producing a plasma display panel including: forming a first electrode on a first substrate; forming a dielectric glass layer to cover the first electrode; forming a protective film to cover the dielectric glass layer, the protective film comprising magnesium oxide (MgO) and an additional oxide, said additional oxide comprising an element with an electronegativity of 1.4 or higher and having a negative charge including silicon oxide; wherein the process of forming the protective film is vacuum evaporation (paragraph 38).

Regarding claim 6, Kato Daito ('153) teaches the second electrode is positioned orthogonally to the first electrode.

Regarding claim 7, Daito ('153) teaches a second electrode on a second substrate, wherein the first electrode and the second electrode are arranged orthogonally to each other.

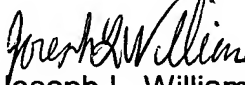
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**Contact Information**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Joseph L. Williams whose telephone number is (571) 272-2465. The examiner can normally be reached on M-F (6:30 AM-3:00 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nimeshkumar D. Patel can be reached on (571) 272-2457. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

  
Joseph L. Williams  
Primary Examiner  
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